



SHREE L. R. TIWARI COLLEGE OF ENGINEERING

An Autonomous Institute Affiliated to University of Mumbai.
Approved by AICTE & DTE, Maharashtra State [NAAC Accredited, NBA Accredited Program ISO 9001:2015 Certified]
DTE Code No. 34231 Recognized under Section 2(f) of the UGC Act 1956 [Minority Status (Hindi Linguistic)]

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING TIME TABLE (Semester - V) W.E.F. 21th JULY, 2025

Academic Year: 2025-2026

Course TE: ELECTRONICS AND TELECOMMUNICATION ENGINEERING(408)

Academic Year: 2025-2026				Course TE: ELECTRONICS AND TELECOMMUNICATION ENGINEERING-4007				
TIME →								
DAYS↓	9:15 - 10:15	10:15 - 11:15	11:15 - 12:15	12:15 - 12:45	12:45 - 1:45	1:45 - 2:45	2:45 - 3:45	3:45 - 4:45
Monday	PCE-II AG	RSA ANM	DVLSI SM	LUNCH BREAK	PCEL-B1-AG-401		HONOUR/MINOR COURSE SLOT(408)	
					DCOML-B2-NNK-411			
					DTSPL-B3-ACM-404B			
Tuesday	MENTORIN G/LIBRARY	DCOM NNK	DVLSI SM		DTSP ACM	DSA USB	DCOML-B1-SMP-411	
							DTSPL-B2-ACM-404B	
							DVLSIL-B3-SM-312	
Wednesday	DVLSI SM	DTSP ACM	DCOM NNK		PCE-II AG	RSA(TUT) ANM	DSA USB	SKILL DEVELOPME NT
Thursday	RSA ANM	DVLSIL-B1-SM-410			DCOM NNK	DSA USB	MINI-PROJE CT(MP)(411)	T& P Slot
		PCEL-B2-AG-401						
		DCOML-B3-NNK-411						
Friday	AUDIT COURSE	DTSPL-B1-ACM-404B		RSA ANM	DTSP ACM	HONOUR/MINOR COURSE SLOT(408)		
		DVLSIL-B2-SM-410						
		PCEL-B3-AG-401						

Curriculum Implementation of Third Year Electronics & Telecommunication Engineering Course

Sub Code	Abbreviation	Subject Name	Theory	Practical/ Tut	Name of the Faculty	Batch -1 Faculty	Batch -2 Faculty	Batch -3 Faculty
ETC501	DCOM	Digital Communication	3		Mrs. Namrata kulkarni(NNK)			
ETC502	DTSP	Discrete Time Signal Processing	3		Mrs. Aparna Majare(ACM)			
ETC503	DVLSI	Digital VLSI	3		Dr. Sheetal Mahadik(SM)			
ETC504	RSA	Random Signal Analysis	3	1(TUT)	Mrs. Aboli Moharil (ANM)			
ETC554	DSA	Data Structures and Algorithms	4		Dr. Umesh Bhadade(USB)			
ETL506	DCOML	Digital Communication Lab		2		Mrs. Namrata kulkarni(NNK)	Mrs. Swapna Patil(SMP)	Mrs. Namrata kulkarni(NNK)
ETL507	DTSP	Discrete Time Signal Processing Lab		2		Mrs. Aparna Majare(ACM)	Mrs. Aparna Majare(ACM)	Mrs. Aparna Majare(ACM)
ETL508	DVLSIL	Digital VLSI Lab		2		Dr. Sheetal Mahadik(SM)	Dr. Sheetal Mahadik(SM)	Dr. Sheetal Mahadik(SM)
ETL509	PCEL	Professional Communication & Ethics - II Lab		2*+2	Ms. Amee Gadhia(AG)	Ms. Amee Gadhia(AG)	Ms. Amee Gadhia(AG)	Ms. Amee Gadhia(AG)
ETM 510	MP	Mini Project 2A Embedded System		2	Mrs. Aboli Moharil (ANM)			

Prepared By

N. Kulkarni
19/7/25

Mrs. Namrata Kulkarni
Time-Table Coordinator

Approved By

S. Patil
19/7/25

Mrs. Swapna Patil
H.O.D.