Shree Rahul Education Society's (Regd.)

SHREE L. R. TIWARI
COLLEGE OF ENGINEERING

(Approved by AICTE & DTE, Maharashtra State & Affiliated to University of Mumbai)
NAAC Accredited, NBA Accredited Program, ISO 9001:2015 Certified | DTE Code No.: 3423
Minority Status (Hindi Linguistic)

DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION

NEWSLETTER

The objectives of this News Letter is to keep our Students, Parents, Faculty and Industry informed about the activities happening in the Department.

Vision

To be recognized by the society as one of the global leaders in the field of Electronics and Telecommunication Engineering. To contribute in the holistic growth of students and make them competent to contribute to the need of public and society through research, innovation and technological advancement.

Mission

To deliver theoretical based, advanced technological concepts, teamwork spirit, ethics, human values, practical base, research and development to the students, extension activities to other organizations through creation of advanced facilities and providing platforms for synergy.

To create opportunities for Research and Development to meet the ever changing and ever demanding needs of the Electronics industry in particular, along with other interdisciplinary fields in general.

To promote research culture by infusing scientific temper in the students and guiding them towards R&D activities.

Message from HOD of Department

Mrs. Swapna Patil
Assistant Professor
(HOD EXTC)

The global electronics industry is vast. Electronics is so ubiquitous in modern life that there are many opportunities for electronic engineering graduates to apply their expertise in other industry sectors. Business, medicine, aviation, communications, transport, space exploration, industrial automation, science, entertainment, music, gaming and environmental monitoring have all been revolutionized by electronics. The role of the electronics engineer is pivotal in realms ranging from the toy industry to consumer electronics, from household articles to space/satellite communication. Electronic engineering covers subfields such as analog & digital electronics, embedded

systems and power-electronics. Electronics engineering deals with design and implementation of various applications, be it from telecommunication, or automobile, or production, or bio-medical, the list is endless.

SLRTCE has spacious and well-ventilated classrooms with fully equipped laboratories and a huge Central Library. The institute has a fully-fledged Placement Cell with 100% Placement Assistance. The Incubation Centre, 'Vyavasay Abhyas Niketan', provides opportunities for students to work on various funded projects. The Entrepreneurship Cell (E-Cell), engages students in Group Discussions, Workshops, Presentations and participation at various events, which helps build their confidence and enhance their personality as budding entrepreneurs.

Value added training sessions help students as well as teachers to hone pertinent skills in all domains. Teachers of every Department are encouraged to participate in Workshops at various Institutes and also to organize STTPs, Seminars and Activities throughout the year at SLRTCE.

Besides the regular lecture method, teachers adopt learner centric techniques to involve students actively through Course Networking and NPTEL Courses. The mentoring system, Peer Learning, Group Discussion, Case Studies, Project Reports and Quizzes also form an integral part of the teaching methodology.

Courses Offered

Undergraduate Courses	Annual Intake (UG)
Electronics and Telecommunication Engineering (EXTC)	60
Computer Engineering (CMPN)	180
Mechanical Engineering (MECH)	30
Electronics and Computer Science Engineering	60
Civil Engineering (CIVIL)	30
Information Technology (IT)	180
Post- Graduate Courses	**Annual Intake (PG)**
Electronics and Telecommunication Engineering (EXTC)	12
Computer Engineering(CMPN)	12

Program Educational Objectives

- To produce Electronics & Telecommunication engineers, having strong theoretical foundation, good design experience and exposure to research and development.
- To produce researcher who have clear thinking, articulation and interest to carry out theoretical and/or applied research resulting in significant advancement in the field of specialization.
- To develop an ability to identify, formulate and solve electronics and telecommunication engineering problems in the latest technology.

- To develop the ability among students to synthesize data and technical concepts from applications to product design.

Events

A "Yoga Session to relax, rejuvenate, and refresh mind, body, and soul!" was organized by ECS department for all the SE, TE and BE students of EXTC and ECS on 10th February, 2025 at 9:15 AM *(1hour).* The session was conducted by the trainer **Sulbha Shelar and Santosh Khatavkar From Sanskar Yog** The objective of this workshop was to experience the transformative power of yoga as they explore balance, harmony, and well-being on physical, mental, and emotional levels.

A workshop on **"Hands On training on latest FPGA boards"** was conducted by EXTC

department for TE EXTC students on 15th February 2025 from 10:00 AM - 4:00 PM which was attended by 20 students The speakers for the workshop were Mr.Aryan Ambre (BE EXTC student) along with Mr.Jagdish Choudhary (TE EXTC). The workshop provided a valuable learning experience for students interested in FPGA technology. By working with Xilinx tools and Verilog, they gained practical skills in hardware programming and debugging. The hands-on training with Spartan 3A and Spartan 3AN boards helped Students Bridge the gap between theoretical knowledge and real-world applications. The session was interactive, with students actively participating in coding exercises and problem-solving tasks. Overall, the seminar successfully enhanced students' understanding of FPGA-based systems and encouraged them to explore further opportunities in this field. Furthermore, the completion of the Half Adder project gave students practical exposure to designing and implementing fundamental digital circuits on FPGA hardware

A Workshop on **"Arduino on Cloud"** was

conducted for all the SE EXTC students on 21st February 2025 from 10:00 am - 12:00 pm. The session was organized by Mini project 1B coordinator Mrs. Maheshwari Jadhav. A total of 32 students participated in the session. The speaker for the session was **Mr. Rajesh Kushalkar, Sr. Project Manager at IIT Bombay.** The workshop successfully provided students with valuable insights into open-source hardware, software, and IoT applications. Participants gained hands-on experience with Arduino and cloud integration, learning how to transform ideas into real-world products.

An "Industrial Visit to IIT Bombay Aerospace Engineering Department of prestigious Institute of Technology, IIT Bombay" was organized by the EXTC & department on 12th March 2025. The EXTC students along with the EXTC faculties Mrs.

Swapna Patil, Mrs. Aboli Moharil, Mrs. Aparna Majare, Mrs. Maheshwari Jadhav & Mr. Aditya Desai respectively visited IIT Bombay, at 10:00 am.

The objective of the session was to observe and understand various aerospace technologies and their applications. To enhance students learning experience by interacting with experts in the field. To draw inspiration from industry experts and apply gained knowledge in academic projects and future careers World. The industrial visit to IIT Bombay was a highly informative and enriching experience for the students. It provided valuable exposure to advanced engineering concepts and allowed them to observe real-world applications of their coursework. The visit helped in bridging the theoretical and practical aspects of aerospace engineering, fostering a deeper understanding of the subject

Student from TE EXTC Mr. Rishabhkumar Dubey cleared examination of National Cadet Corps (NCC) with 'A'(AAA) grade under the authority of Ministry of Defence, Government of India. Anurag Mishra

Faculties of EXTC department Mrs. Maheshwari Jadhav, Mrs. Shruti Patil, Mrs.Namrata Kulkarni & Mrs. Menka Singh got an opportunity to deliver session for Indian Navy at INS HAMLA for the IT Manager Course (ITMC) for the Networking C& C++ subject. Topics covered by Mrs. Maheshwari Jadhav were IPv4, IPv4 addressing subnetting IPv6, NAT, routing protocols & by Mrs. Shruti Patil were: Networking Topologies, Network Devices Configuration (Configuration of Router and switch using CISCO Packet Tracer), and Introduction to Virtual LAN (Configuring VLAN using CISCO Packet Tracer). Mrs. Namrata Kulkarni & Shruti Patil covered the topics related to basics of C & C++ programming. Mrs. Menka Singh covered the topics related to Advance Networking